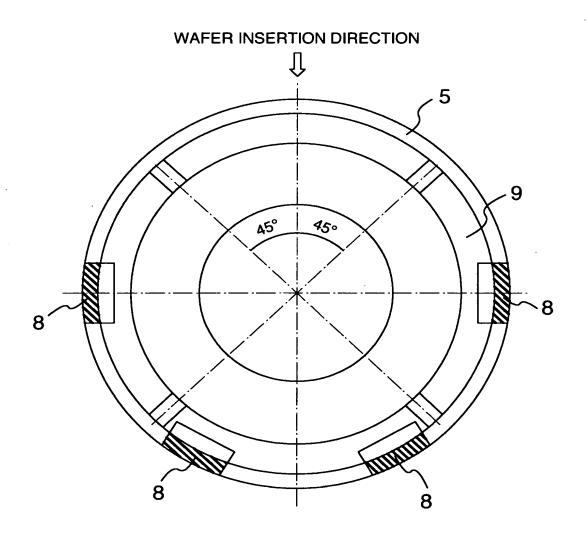


FIG. 3



The state of the s

FIG. 4

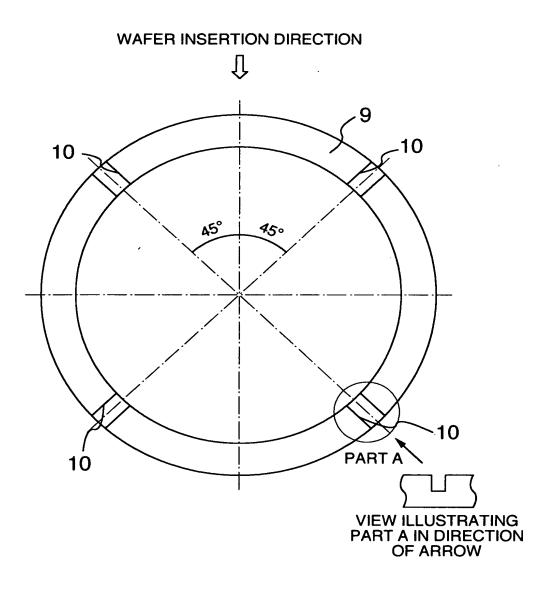
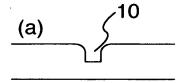
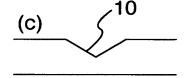
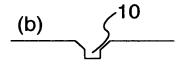
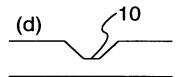


FIG. 5









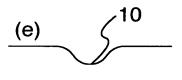


FIG. 6

WAFER INSERTION DIRECTION $\begin{picture}(1,0) \put(0,0){\line(0,0){100}} \put(0,0){\line(0,0){100$

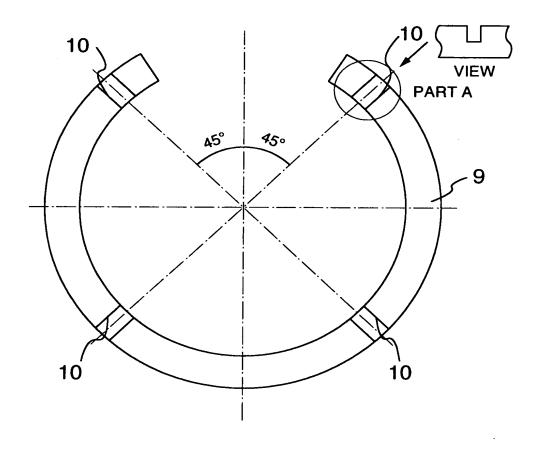


FIG. 7

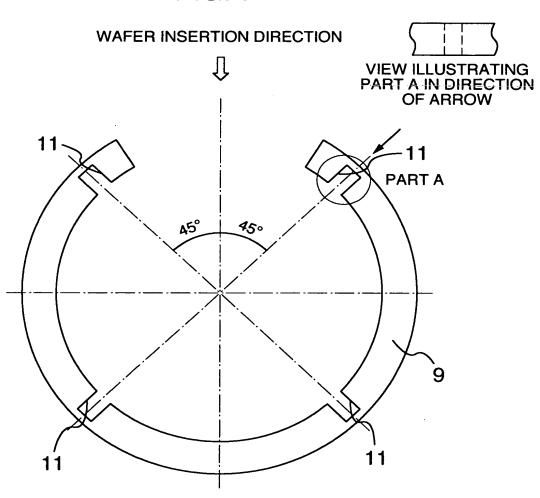


FIG. 8

WAFER INSERTION DIRECTION

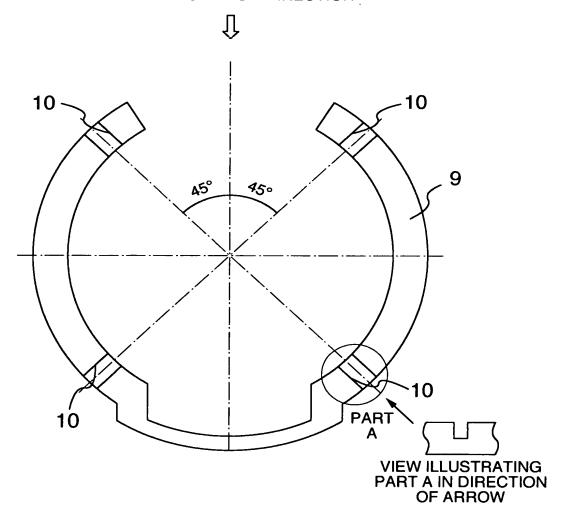


FIG. 9

WAFER INSERTION DIRECTION

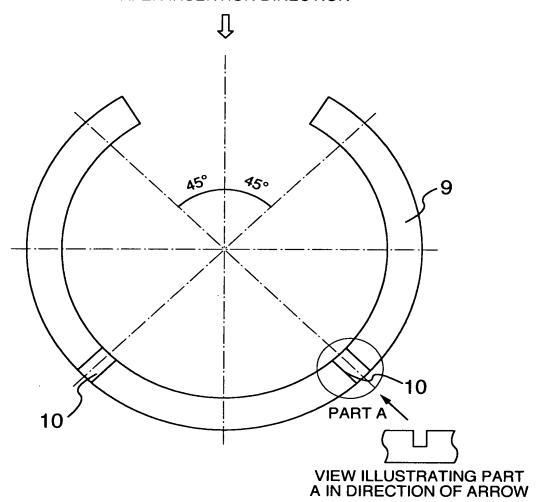


FIG. 10

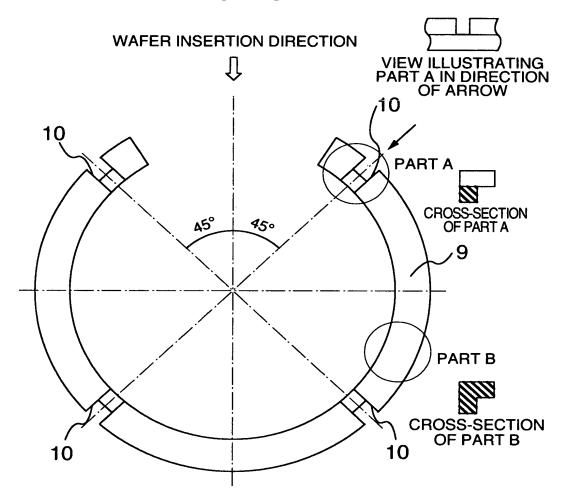


FIG. 11

WAFER INSERTION DIRECTION

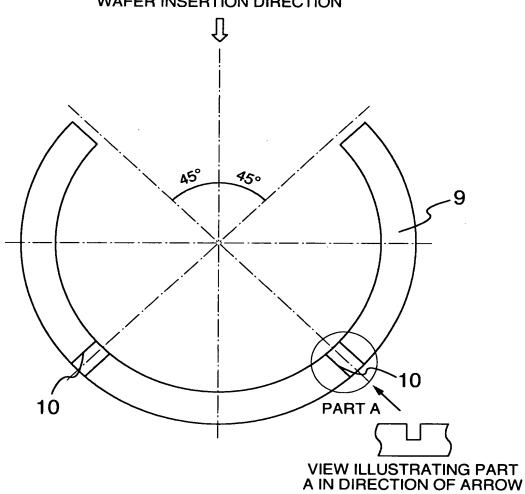
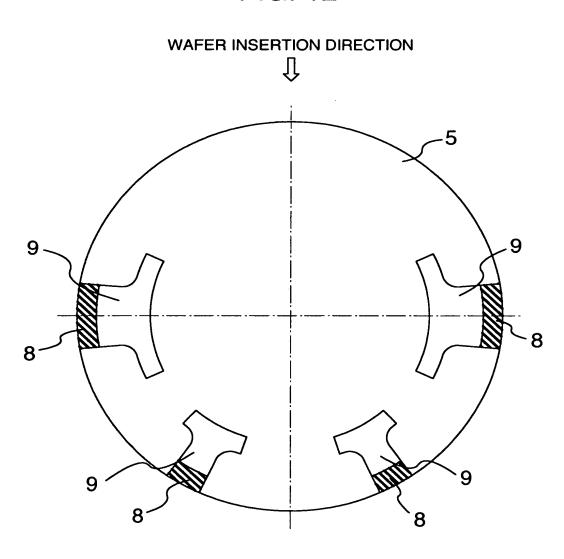


FIG. 12



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